

WEST Search History

DATE: Wednesday, May 14, 2003

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT,PGPB,JPAB; PLUR=YES; OP=OR

L57	L56 and l8	6	L57
L56	simulat\$ same (execut\$ same rate)	520	L56
L55	L54 and (execut\$ same rate)	44	L55
L54	L52 and (emulat\$ or simulat\$)	99	L54
L53	L52 and quantum	2	L53
L52	L43 and varian\$3 and conform\$ and load and instruction	192	L52
L51	L50 and quantum	1	L51
L50	L49 and l43	99	L50
L49	L4 and varian\$3 and conform\$ and load and instruction	290	L49
L48	L47 and varian\$3	32	L48
L47	L45 and emulat\$ and conform\$	106	L47
L46	l36 and l45	10	L46
L45	l43 and L44	1759	L45
L44	((non same native) or target) same (code or instruction)	26315	L44
L43	timing same reference	85117	L43
L42	L40 and instruction	32	L42
L41	L40 and timing and instruction	11	L41
L40	L36 and bench\$ and (emulat\$ or simulat\$)	63	L40
L39	L38 and l36	7	L39
L38	bench\$ and (emulator or simulator)	801	L38
L37	l35 and L36	1	L37
L36	conform\$ same (variant or variance or variation)	13002	L36
L35	bench\$ and (emulator or simulator) and target	384	L35
L34	l27 and varian\$3	40	L34
L33	L28 and wedge	3	L33
L32	L31 and wedge	0	L32
L31	L30 and (dis\$3 same drive)	90	L31
L30	L29 and register	128	L30
L29	L28 and driver	191	L29
L28	fault same toleran\$ same mirror\$	458	L28
L27	l25 and l19	108	L27
L26	L25 and bench\$ and speed and cycle	236	L26

L25	instruction same (emulat\$ or simulat\$)	8154	L25
L24	L22 and adjust\$ and (block same instruction)	43	L24
L23	L22 and adjust\$ and block	273	L23
L22	L21 and (cpu or processor)	332	L22
L21	L20 and cycle and (time or timing or clock)	385	L21
L20	L19 and (variant or variance or variation)	441	L20
L19	bench\$ and (emulat\$ or simulat\$) and speed and conform\$ and target	530	L19
L18	L8 and speed and bench\$	4	L18
L17	L8 and target and bench\$	2	L17
L16	L15 and emulat\$ and time	5	L16
L15	5796633	37	L15
L14	l8 and L13	48	L14
L13	target same performance	18068	L13
L12	L11 not l10	27	L12
L11	L9 and adjust\$	56	L11
L10	L9 and (adjust\$ same (speed or execution))	29	L10
L9	l8 and l4	80	L9
L8	((conform\$ same (variant or variance or variation or speed or execution))and ((emulat\$ or simulat\$) same instruction))	215	L8
L7	L5 and adjust\$	31	L7
L6	L5 and (adjust\$ same speed)	0	L6
L5	l3 and L4	43	L5
L4	clock same cycle	68419	L4
L3	l2 and cycle	70	L3
L2	((conform\$ same (variant or variance or variation or speed or execution))and ((emulat\$ or simulat\$) same instruction)) and target and timing)	75	L2
L1	((((target\$3 same (cpu or processor or coprocessor))and block) and (cycle or cycled))	7899	L1

END OF SEARCH HISTORY